

C.U.SHAH UNIVERSITY

Summer Examination-2018

Subject Name: Advanced Micro-Processors

Subject Code: 4TE05AMP1

Branch: B.Tech (CE,IT)

Semester: 5

Date: 31/03/2018

Time: 10:30 To 1:30

Marks: 70

Instructions:

- (1) Use of Programmable calculator & any other electronic instrument is prohibited.
- (2) Instructions written on main answer book are strictly to be obeyed.
- (3) Draw neat diagrams and figures (if necessary) at right places.
- (4) Assume suitable data if needed.

Q-1

Attempt the following questions:

- | | | |
|----|--|------|
| a) | Define: Microprocessor. | (01) |
| b) | Define: Instruction cycle. | (01) |
| c) | Define: Microcontroller. | (01) |
| d) | What is instruction Queue? | (01) |
| e) | What is maskable interrupt? | (01) |
| f) | What is addressing mode? | (01) |
| g) | True/False: 8085 is 1 byte microprocessor. | (01) |
| h) | The size of each segment in 8086 is:
A) 8 KB B) 64 KB C) 64 bit D) 8 bit | (01) |
| i) | The processor 80386/80486 and the Pentium processor uses _____ bits address bus:
A) 16
B) 32
C) 36
D) 64 | (01) |
| j) | STA 2000 instruction is how many byte of instruction?
A) 4
B) 2
C) 1
D) 3 | (01) |
| k) | The CF is known as _____
A) carry flag B) condition flag C) common flag D) single flag. | (01) |
| l) | Which is not the control bus signal?
A) Read B) Write C) Reset D) None of these. | (01) |
| m) | How many bits the instruction pointer is wide? A) 16 B) 8 C) 32 D) 64 | (01) |
| n) | In which mode 80386 support memory management and protection mechanism?
A. Direct mode B. indirect mode
C. Protected mode D. Real mode | (01) |

Attempt any four questions from Q-2 to Q-8

Q-2

Attempt all questions

(14)



