	Enrollm	ent No	:						
			C.U.SHA	AH UNIVERSIT	$\mathbf{Y}$				
			Summer	Examination-2018	}				
	Subject Name: Advanced Micro-Processors								
	Subject	Code:	4TE05AMP1	Branch: B.Tech (CE,IT)					
	Semeste Instruction		Date: 31/03/2018	Time: 10:30 To 1:30	Marks: 70				
	ent is prohibited. ed.								
Q-1		Atte	empt the following que	estions:					
	<b>a</b> )		ne: Microprocessor.		(				
	<b>b</b> )		ne: Instruction cycle.						
	c)		ne: Microcontroller.						
	<b>d</b> )		at is instruction Queue? at is maskable interrupt		(				
	e) f)		at is addressing mode?	:	(				
	<b>g</b> )		e/False: 8085 is 1 byte r	microprocessor					
	h)		size of each segment in						
	,		KB B) 64 KB C) 64 l		(				
	i)			6 and the Pentium processor uses _	bits address				
		bus:							
		A) 1			(				
		B) 3			,				
		C) 3							
	• \	D) 6		1					
	j)			w many byte of instruction?					
		A) 4 B) 2			,				
		C) 1			(				
		C) 1							

g)	True/Faise: 8085 is 1 byte microprocessor.	(01)		
h)	The size of each segment in 8086 is:	(01)		
	A) 8 KB B) 64 KB C) 64 bit D) 8 bit	(01)		
i)	The processor 80386/80486 and the Pentium processor uses bits address			
	bus:			
	A) 16	(01)		
	B) 32	(01)		
	C) 36			
	D) 64			
<b>j</b> )	STA 2000 instruction is how many byte of instruction?			
	A) 4			
	B) 2	(01)		
	C) 1			
	D) 3			
k)	The CF is known as			
	A) carry flag B) condition flag C) common flag D) single flag.	(01)		
1)	Which is not the control bus signal?	(01)		
	A) Read B) Write C) Reset D) None of these.	(01)		
m)	How many bits the instruction pointer is wide? A) 16 B) 8 C) 32 D) 64	(01)		
n)	In which mode 80386 support memory management and protection mechanism?			
	A. Direct mode B. indirect mode	(01)		
	C. Protected mode D. Real mode			



**(14)** 

Attempt any four questions from Q-2 to Q-8 Q-2 Attempt all questions



	(a)	Draw and Explain general segment descriptor format for x86 family processors.	
	<b>(b)</b>	Write a short note on Descriptor table.	
Q-3		Attempt all questions	(14)
	(a)	Draw and Explain Architecture of 8086 Microprocessor.	
	<b>(b)</b>	Explain different types of instruction of 8085 Microprocessor.	
Q-4		Attempt all questions	(14)
	<b>(a)</b>	Explain Real Address Mode and Protected Virtual Address Mode of 80286 MP.	
	<b>(b)</b>	Draw and Explain Register set of 80386 Microprocessor.	
Q-5		Attempt all questions	(14)
	<b>(a)</b>	Explain following instruction: LEA, HLT, MOV, XCHG, NEG, RCL,CMP	
	<b>(b)</b>	Write a short note on Memory bank.	
Q-6		Attempt all questions	
	(a)	Explain various addressing modes of 8086 Microprocessor.	<b>(07</b> )
	<b>(b)</b>	Explain segmentation in 8086 Microprocessor	(05)
	(c)	Differentiate 8 bit and 16 bit Microprocessor.	(02)
Q-7		Attempt all questions	(14)
	(a)	Draw pin diagram of 8085 Microprocessor and explain Interrupt related pin.	
	<b>(b)</b>	What is Gate? Explain general Gate Descriptor format.	
Q-8		Attempt all questions	
•	<b>(a)</b>	Explain Demultiplexing process of AD7- AD0 bus system in 8085	<b>(7)</b>
		Microprocessor.	
	<b>(b)</b>	Enlist and explain features of Pentium MMX processor	<b>(5)</b>
	(c)	Differentiate between RISC and CISC.	(2)

